

ABSTRACT OF THE DISCLOSURE

A control circuit for a semiconductor device with overheat protecting function is provided, which includes: a semiconductor element; an overheat protecting means; a chip to mount the semiconductor element and the overheat protecting means; a control means to supply a pulse-width modulation control signal having a fixed pulse width to the semiconductor element; and an outputting state detecting means to detect abnormality of output of the semiconductor device with overheat protecting function during an overheat protective operation of the overheat protecting means, wherein the control means monitors detection output from the outputting state detecting means at a fixed monitoring timing and stops supplying the pulse-width modulation control signal to the semiconductor element when the detection output is generated successively predetermined times or successively during predetermined time.

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